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1374.39158CX1

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ANTONELLI, TERRY STOUT & KRAUS, LLP
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Ricardo E. Perez (Depositor's name)
Ricardo Perez (Signature)
12/05/2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/770,413	02/04/2004	Norio Hasegawa	1374.39158CX1	1925
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TITLE OF INVENTION: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICES

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE (S) DUE	DATE DUE
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Nonprovisional	NO	\$1400	\$300	\$1700	12/05/2006
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EXAMINER	ART UNIT	CLASS-SUBCLASS
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CHACKO DAVIS, DABORAH	1756	430-394000
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(A) NAME OF ASSIGNEE

RENESAS TECHNOLOGY CORP.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TOKYO, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature *Leonid D. Thenor*
Typed or printed name **Leonid D. Thenor**

Date: **DECEMBER 5, 2006**
Registration No. **39,397**

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OMB 0851-0033

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01 FC:1501	1400.00 OP
02 FC:1504	300.00 OP
03 FC:8001	6.00 OP